

Claim Amendments

1-11. (Canceled)

12. (Currently Amended) An LGA interconnect, for interconnection to further electrical components, said LGA interconnect comprising:

a substrate having an array of contact receiving openings therein, and the substrate having a receiving aperture, the receiving aperture having a receiving portion which transitions into a locking portion,

a plurality of contact assemblies positioned and retained in said substrate;

a frame housing positioned around a periphery of said substrate; and

alignment members projecting from said substrate, and extending through said frame housing, for aligning said substrate relative to at least one of the electrical components, the alignment members being insertable into said receiving portion ~~opening~~ and locked in place to said substrate by said locking portion.

13. (Original) The LGA interconnect of claim 12, wherein said alignment members are comprised of pins.

14. (Original) The LGA interconnect of claim 13, wherein said pins are attached at diametrically opposite positions of said substrate.

15. (Original) The LGA interconnect of claim 13, wherein said frame housing can laterally float relative to said pins.

16. (Original) The LGA interconnect of claim 15, wherein said frame housing is comprised of first and second frame parts attached to each other.

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17. (Original) The LGA interconnect of claim 13, wherein said pin is substantially cylindrical.

18. (Currently Amended) The LGA interconnect of claim 17, wherein said receiving portion and said locking portion are substantially circular.

19. (Original) The LGA interconnect of claim 18, wherein said pin is rotated into said locked position.

20. (Currently Amended) The LGA interconnect of claim 19, wherein said pin includes an undercut groove, which, when aligned with said locking portion, allows said transverse movement of said pin into said locking portion.

21. (Original) The LGA interconnect of claim 20, wherein said receiving aperture includes a channel section at the intersection of said receiving portion and said locking portion.

22. (Original) The LGA interconnect of claim 21, wherein said undercut groove includes flat edge portions defined by parallel chordal edges, profiled to be received in said channel section.

23. (Original) The LGA interconnect of claim 22, wherein said channel section is provided by straight edges extending between said receiving portion and said locking portion.

24. (Original) The LGA interconnect of claim 23, wherein one of said straight edges includes a transverse stop edge.

25. (Original) The LGA interconnect of claim 24, wherein an edge opposite said transverse stop edge is provided with a latch member, whereby when said pin is rotated to a position where one of said chordal edges abuts said transverse stop edge, the other of said chordal edges is locked in position against said latch member, and said pin is held longitudinally fixed to said substrate.

26. (Currently Amended) The LGA interconnect of claim 25, wherein said latch member is defined by an elongate latch member, etched from said substrate and extending part way into said locking portion.

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